

1 51798/RJP/B600 - BP1199CON2

5 GENERALIZED CONVOLUTIONAL
INTERLEAVER/DEINTERLEAVER

ABSTRACT OF THE DISCLOSURE

10 A memory-efficient convolutional interleaver/deinterleaver
with a memory array, a write commutator, and a read commutator
wherein the commutators perform their respective write and read
operations relative to a preselected memory cell after a
predetermined delay. The delay is chosen using a modulo-based
technique, such that an efficient implementation of a Ramsey
Type-II interleaver is realized.

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